

Fig. 2

500 -Control Parameters **HSDPA** TX Enable from ARM 515 505 520 525 510 -ΤX TX TO RF DPDCH/ (DPCCH/HS-DPCCH) I/Q MODEM DPDCH/ DPCCH I/Q COPROCESSOR

Fig. 5

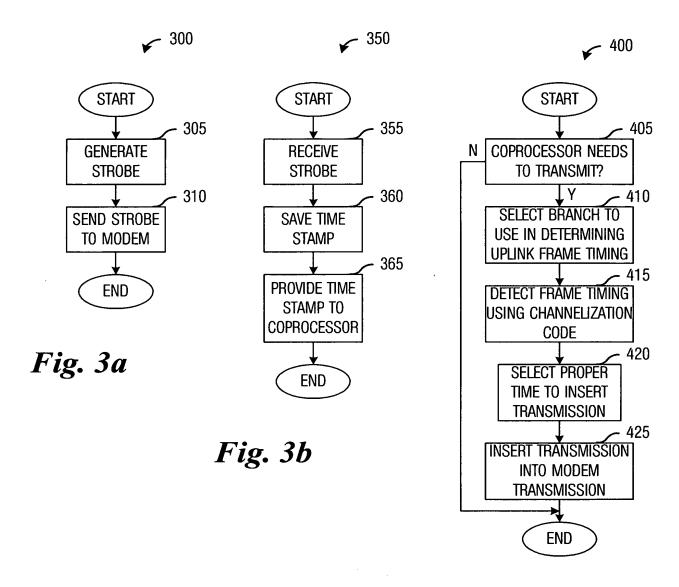


Fig. 4

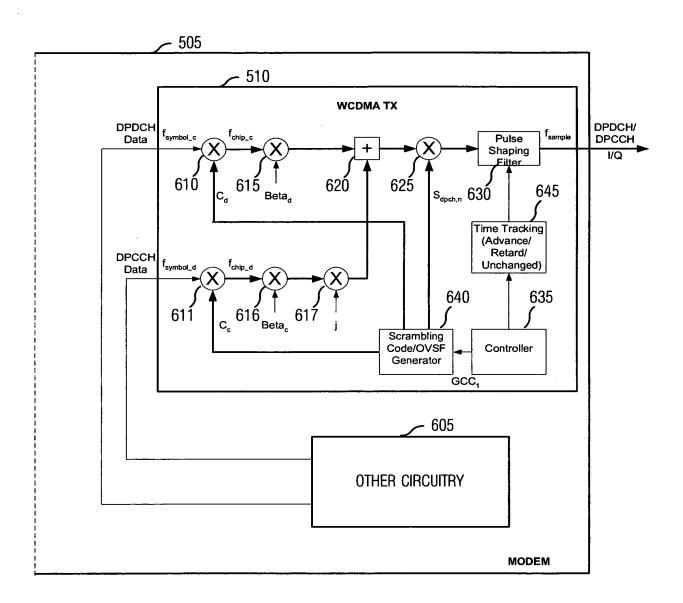


Fig. 6

